

88-8 system \rightarrow $\underbrace{R_7 \ R_6 \ R_5 \ R_4 \ R_3}_{5-6-5 \text{ system}} \underbrace{R_2 \ R_1 \ R_0}_{\text{truncated}}$

8-8-8 \rightarrow $\underbrace{G_7 \ G_6 \ G_5 \ G_4 \ G_3 \ G_2}_{5-6-5 \text{ system}} \underbrace{G_1 \ G_0}_{\text{truncated}}$

8-8-8 \rightarrow $\underbrace{B_7 \ B_6 \ B_5 \ B_4 \ B_3}_{5-6-5 \text{ system}} \underbrace{B_2 \ B_1 \ B_0}_{\text{truncated}}$

FIG. 1



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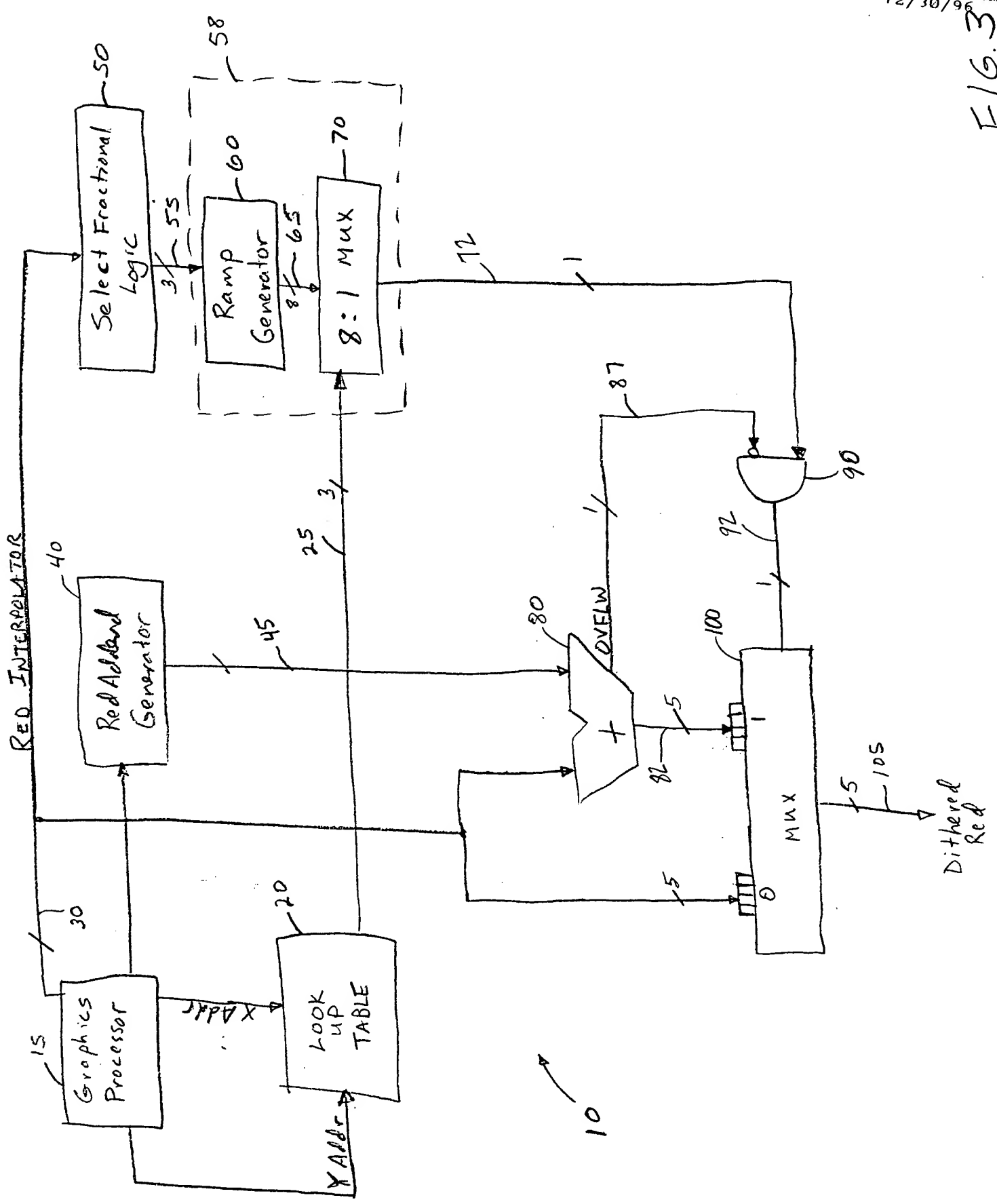
8 BIT

5 BIT

	7	6	5	4	3	2	1	0	4	3	2	1	0
	0	0	0	0	0	0	0	0	0	0	0	0	0
A { 1/8	0	0	0	0	0	0	0	1	0	0	0	0	0
2/8	0	0	0	0	0	0	1	0	0	0	0	0	0
3/8	0	0	0	0	0	0	1	1	0	0	0	0	0
4/8	0	0	0	0	0	1	0	0	0	0	0	0	0
5/8	0	0	0	0	0	1	0	1	0	0	0	0	0
6/8	0	0	0	0	0	1	1	0	0	0	0	0	0
7/8	0	0	0	0	0	1	1	1	0	0	0	0	0
	0	0	0	0	1	0	0	0	0	0	0	0	1
A { 1/8	0	0	0	0	1	0	0	1	0	0	0	0	1
2/8	0	0	0	0	1	0	1	0	0	0	0	0	1
3/8	0	0	0	0	1	0	1	1	0	0	0	0	1
4/8	0	0	0	0	1	1	0	0	0	0	0	0	1
5/8	0	0	0	0	1	1	0	1	0	0	0	0	1
6/8	0	0	0	0	1	1	1	0	0	0	0	0	1
7/8	0	0	0	0	1	1	1	1	0	0	0	0	1
	0	0	0	1	0	0	0	0	0	0	0	1	0
1/8	0	0	0	1	0	0	0	1	0	0	0	1	0
					.						.		
					.						.		
					.						.		

FIG. 2

FIG. 3



		XAddr							
		0	1	2	3	4	5	6	7
Y A d d r	0	0	4	1	5	0	4	1	5
	1	6	2	7	3	6	2	7	3
	2	1	5	0	4	1	5	0	4
	3	7	3	6	2	7	3	6	2
	4	0	4	1	5	0	4	1	5
	5	6	2	7	3	6	2	7	3
	6	1	5	0	4	1	5	0	4
	7	7	3	6	2	7	3	6	2

FIG. 4



61 →

FRAC	RAMP (5 bit system)						
	7	6	5	4	3	2	1
0	0	0	0	0	0	0	0
1	1	0	0	0	0	0	0
2	1	1	0	0	0	0	0
3	1	1	1	0	0	0	0
4	1	1	1	1	0	0	0
5	1	1	1	1	1	0	0
6	1	1	1	1	1	1	0
7	1	1	1	1	1	1	1

62

FIG. 5

64 →

FRAC	RAMP (6 bit system)			
	3	2	1	0
0	0	0	0	0
1	1	0	0	0
2	1	1	0	0
3	1	1	1	0

FIG. 6

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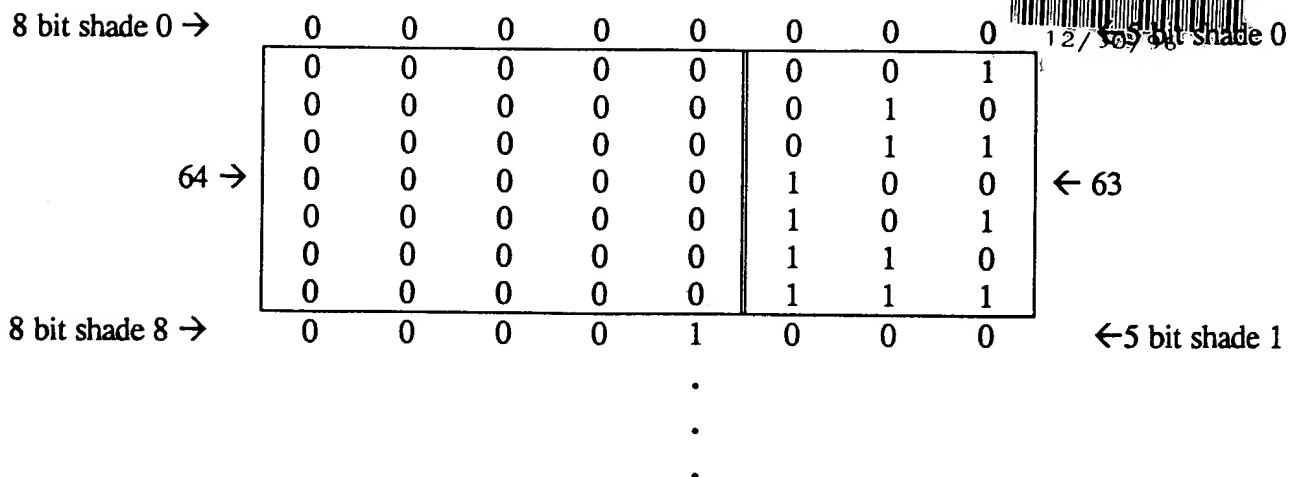


FIG. 7